

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Eiji HASUNUMA, et al.

Serial No.: 09/227,935

Filed: January 11, 1999

For: SEMICONDUCTOR DEVICE AND MANUFACTURING METHOD THEREOF

Box AF
THE COMMISSIONER OF PATENTS AND TRADEMARKS
Washington, DC 20231

Dear Sir:

Transmitted herewith is an Amendment in the above identified application.

- ☐ No additional fee is required.
☐ Small entity status of this application under 37 CFR 1.9 and 1.27 has been established by a verified statement previously submitted.
☒ A verified statement to establish small entity status under 37 CFR 1.9 and 1.27 is enclosed.
☒ Also attached: Petition For Extension of Time

The fee has been calculated as shown below:

	NO. OF CLAIMS	HIGHEST PREVIOUSLY PAID FOR	EXTRA CLAIMS	RATE	FEE
Total Claims	6	20	0	\$18.00 =	\$0.00
Independent Claims	2	3	0	\$78.00 =	\$0.00
Multiple claims newly presented					\$0.00
Fee for extension of time					\$110.00
					\$0.00
Total of Above Calculations					\$110.00

- ☒ Please charge my Deposit Account No. 500417 in the amount of \$110.00. An additional copy of this transmittal sheet is submitted herewith.
- ☒ The Commissioner is hereby authorized to charge payment of any fees associated with this communication or credit any overpayment, to Deposit Account No. 500417, including any filing fees under 37 CFR 1.16 for presentation of extra claims and any patent application processing fees under 37 CFR 1.17.

Respectfully submitted,

MCDERMOTT, WILL & EMERY

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Gene Z. Robinson
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PATENT



: Response Under 37 CFR 1.116 - Expedited Procedure

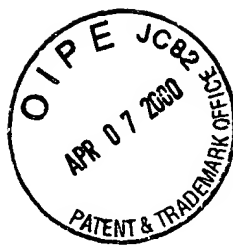
: Group Art Unit: 2811

: Examiner: S. Loke

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TC 2800 MAIL ROOM

Docket No.: 49657-274



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

: **RESPONSE UNDER 37 CFR 1.116**

Eiji HASUNUMA, et al.

: **EXPEDITED PROCEDURE**

Serial No.: 09/227,935

: Group Art Unit: 2811

Filed: January 11, 1999

: Examiner: S. Loke

For: SEMICONDUCTOR DEVICE AND MANUFACTURING METHOD THEROF

AMENDMENT UNDER 37 CFR 1.116

Box AF
Assistant Commissioner for Patents
Washington, DC 20231

*Please enter
amendment B,
Loke
5/21/02*

Sir:

The following amendment and remarks are submitted in response to the final Office Action dated December 9, 1999.

IN THE CLAIMS:

Please cancel claim 3 without prejudice or disclaimer.

Please amend claim 1 as follows.

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TC 2800 MAIL ROOM

1. (Twice Amended) A semiconductor device, comprising:

a semiconductor substrate having a main surface;

[7] (fig. 5) *capacitor*
an element isolating region for defining an element forming region on the main surface of

said semiconductor substrate;